Notice of References Cited Application/Control No. 10/051,276 Examiner Nirav Patel Application/Control No. Applicant(s)/Patent Under Reexamination SHIMBO ET AL. Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-			
	С	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
	ı	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	α					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

HOIT ALLI BOOMETTO							
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	Robert Hohne ("A Programmable High Performance processor using the Residue Number System and CMOS VLSI technology", 1989 IEEE, page 41-43).					
	٧						
	w						
	х						

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.